AQ.\I	Docket No. 42390P10071	PATENT 5-
MAY 1	IN THE UNITED STATES PATENT	AND TRADEMARK OFFICE)
	Terry L. Kendall, et al.	Examiner: Gossage, Glenn A
Application No. 09/752,594) Art Unit: 2187
Filed:	December 27, 2000 Method And Apparatus Including Special Programming Mode Circuitry Which Disables Internal Program Verification Operations By A Memory	RECEIVED MAY 1 3 2004 Technology Center 2100
PO Bo	nissioner for Patents ox 1450 ndria, VA 22313-1450	
	AMENDMI	<u>ENT</u>

Sir:

In response to the Office Action dated February 5, 2004, Applicant respectfully requests that the Examiner enter the following amendments and consider the following remarks.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 6 of this paper.

Remarks/Arguments begin on page 11 of this paper.

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